1-Mar		2-Mar	3-Mar
Session: Opening and Keynote (Chair: Zhe Hou)		Session: Keynote (Chair: Zhe Hou)	Session: Keynote (Chair: Guangdong Bai)
Opening		Program starts at 9am Singapore time today.	Program starts at 9am Singapore time today.
10:00 Keynote: Verified Artificial Intelligence and Autonomy Sanjit Seshia		Keynote: Modular Synthesis of Certifying STV Counting Programs **Rajeev Gore**	Keynote: Towards a Unified Framework for Neural Network Analysis <i>Jun Sun</i>
		Morning Tea	
Tutorial: Silas: Dependable and High Performance Machine Learning	Workshop	Session 1: Program Verification (Chair: David Sanán)	Session 4: Formal Methods for Machine Learning (Chair: Xiaofei Xie)
		Verification of Programs with Pointers in SPARK Georges-Axel Jaloyan, Claire Dross, Maroua Maalej, Yannick Moy, Andrei Paskevich	PAC Learning of Deterministic One-Clock Timed Automata Wei Shen, Jie An, Bohua Zhan, Miaomiao Zhang, Bai Xue, Naijun Zhan
Performance Machine Learning Zhe Hou		Automated Temporal Verification of Integrated Dependent Effects Yahui Song, Wei-Ngan Chin	Learning Fault Models of Cyber Physical Systems Teck Ping Khoo, Jun Sun, Sudipta Chattopadhyay
		Parallel Chopped Symbolic Execution Shikhar Singh, Sarfraz Khurshid	VARF: Verifying and Analyzing Robustness of Random Forests Chaoqun Nie, Jianqi Shi, Yanhong Huang
		Lunch	
	Workshop	Session 2: Programming Languages (Chair: Xuan Bach Le)	Session 5: Applications of Formal Methods (Chair: David Sanán)
Tutorial: Silas: Dependable and High Performance Machine Learning Zhe Hou (cont.)		A Reversible Operational Semantics for Imperative Programming Languages Maribel Fernández, Ian Mackie	Formal Foundations for Intel SGX Data Center Attestation Primitives Muhammad Usama Sardar, Rasha Faqeh, Christof Fetzer
13:30 - 15:00		Type-Based Declassification for Free Minh Ngo, David A. Naumann, Tamara Rezk	Formalizing the Transaction Flow Process of Hyperledger Fabric Xiangyu Chen, Ximeng Li, Qianying Zhang, Zhiping Shi, Yong Guan
Doctoral Symposium		Four-Valued Monitorability of ω-Regular Languages Zhe Chen, Yunyun Chen, Robert M. Hierons, Yifan Wu	Embedding Approximation in Event-B: Safe Hybrid System Design Using Proof and Refinement Guillaume Dupont, Yamine Aït-Ameur, Neeraj K. Singh, Fuyuki Ishikawa, Tsutomu Kobayashi, Marc Pantel
15:00 - 15:30 Afternoon Tea			
15:30 - 17:00 Workshop		Session 3: Safety and Security (Chair: Yuekang Li)	Session 6: Applications and Short Papers (Chair: Yan Zheng)
		Hackers vs. Security: Attack-Defence Trees as Asynchronous Multi-agent Systems Jaime Arias, Carlos E. Budde, Wojciech Penczek, Laure Petrucci, Teofil Sidoruk, Mariëlle Stoelinga	Formally Verified Trades in Financial Markets Suneel Sarswat, Abhishek Kr Singh
		Robustness and Failure Detection in Epistemic Gossip Protocols Kosei Fujishiro, Koji Hasebe	(Short) Towards Modeling and Verification of the CKB Block Synchronization Protocol in Coq Hao Bu, Meng Sun
			(Short) Accurate Abstractions for Controller Synthesis with Non-uniform Disturbances Yunjun Bai, Kaushik Mallik
		Reasoning with Failures Hamid Jahanian, Annabelle McIver	(Short) Formalising Privacy-Preserving Constraints in Microservices Architecture Inna Vistbakka, Elena Troubitsyna
			(Short) Algebraic Approach for Confidence Evaluation of Assurance Cases <i>Yoriyuki Yamagata, Yutaka Matsuno</i>
	Session: Opening and Opening and Opening and Opening and Session: Verified Artificial Sanji Sanji Tutorial: Silas: Dependable and High Performance Machine Learning Zhe Hou Tutorial: Silas: Dependable and High Performance Machine Learning Zhe Hou (cont.) Doctoral Symposium	Session: Opening and Keynote (Chair: Zhe Hou) Opening Keynote: Verified Artificial Intelligence and Autonomy Sanjit Seshia Tutorial: Silas: Dependable and High Performance Machine Learning Zhe Hou Tutorial: Silas: Dependable and High Performance Machine Learning Zhe Hou (cont.) Workshop Doctoral Symposium	Session: Opening and Keynote (Chair: Zhe Hou) Opening Opening Keynote: Verified Artificial Intelligence and Autonomy Sanjit Seshia Keynote: Wodular Synthesis of Certifying STV Counting Programs Rajeev Gore Morning Tea Session 1: Program Verification (Chair: David Sandi) Verification of Programs with Pointers in SPARK Georges-Axel Jaloyan, Claire Dross, Maroux Maalel, Yannick Moy, Andrei Performance Machine Learning Zhe Hou Workshop Research A. Naumana, Transra Rezk Four-Valued Monitorability of ω-Regular Languages Zhe Chen, Yunyun Chen, Robert M. Heirons, Vifan Wu Alternoon Tea Session 3: Safety and Security (Chair: Yuekang U) Hackers vs. Security: Attack-Defencek, Laure Petrucci, Teofil Sidoruk, Morrielle Stoelinga Robustness and Failure Detection in Epistemic Gossip Protocols Kosel Fujishira, Kaji Hasebe

Note: the program is in Singapore time.